



SHEET 1 OF 1

Form PTO 1449  
(Modified)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

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09/772,876

## LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Kouji YOSHIDA

FILING DATE

January 31, 2001

GROUP

2623

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
VB	AA	5,684,569	11/04/1997	A. SUGAYA, et al.	—	—	
VB	AB	5,929,978	07/27/1999	K. MASAKI	—	—	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
VB	AO	9-6017	01/10/1997	JAPAN (with English translation)		
VB	AP	10-172900	06/26/1998	JAPAN (with English Abstract)		X
VB	AQ	10-326734	12/08/1998	JAPAN (with English Abstract)		X
VB	AR	2829666	09/25/1998	JAPAN (submitting cover page and page 2 only and with corr. JP 4-23422)		X
VB	AS	4-23422	01/27/1992	JAPAN ((with unedited computer-generated English translation)		
	AT					
	AU					
	AV					

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

VB	AW	K. SIMON, et al., Microelectronic Engineering, vol. 46, pages 457-460, "OVERLAY BUDGET ANALYSIS FOR THE 100 NM DEVICE GENERATION", 1999				
	AX					
	AY					
	AZ					<input type="checkbox"/> Additional References sheet(s) attached

Examiner

Date Considered

1/4/05

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.